

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY KAKINADA
MTECH 1st & 2nd Semesters Special Supplementary Examinations, March - 2017.

**Discrepancy pertaining to these results are to be submitted on or before 23-12-2017 with following documents at
CE(PG) Office,JNTUK,Kakinada**

SNO	HTNO	SUBCODE	SUBNAME	INTERNALS	EXTERNALS	CREDITS
1	06131D5808	A5801	DESIGN & ANALYSIS OF ALGORITHMS	28	-1	0
2	06131D5808	A5803	COMPUTER COMMUNICATION	37	-1	0
3	06131D5808	B5802	EMBEDDED SYSTEMS	24	-1	0
4	06A01D5812	A5806	DATABASE MANAGEMENT SYSTEMS	35	-1	0
5	06A01D5812	B5802	EMBEDDED SYSTEMS	38	-1	0
6	07991D5711	A5701	DIGITAL SYSTEM DESIGN	32	35	1
7	09A01D4312	C4305	MODERN CONTROL THEORY	32	-1	0
8	09A01D4312	D4305	DIGITAL CONTROL SYSTEMS	36	-1	0
9	09A01D4317	C4302	ANALYSIS OF POWER ELECTRONIC CONVERTERS	32	13	0
10	10232D5801	C5801	DATASTRUCTURES AND ALGORTIHM ANALYSIS	34	6	0
11	10531D5730	C3801	DIGITAL SYSTEM DESIGN	27	-1	0
12	10A91D5830	C5801	DATASTRUCTURES AND ALGORTIHM ANALYSIS	26	10	0
13	10A91D5830	D5802	COMPUTER NETWORKS	21	19	0
14	10A91D5830	D5808	HUMAN COMPUTER INTERACTION	21	28	0
15	10A91D5830	D5809	DATA WARE HOUSING AND DATA MINING	22	27	0
16	10A91D5830	D5810	SECURED DATABASE APPLICATIONS DEVELOPMEN	23	24	0
17	10W31D5803	D5808	HUMAN COMPUTER INTERACTION	20	25	0
18	10W32D5812	D5809	DATA WARE HOUSING AND DATA MINING	20	24	0
19	11231D0406	D0401	ADVANCED OPTIMIZATION TECHNIQUES	29	-1	0
20	11232D5804	C5801	DATASTRUCTURES AND ALGORTIHM ANALYSIS	36	10	0
21	11511D0601	C3801	DIGITAL SYSTEM DESIGN	32	-1	0
22	11511D0604	C3801	DIGITAL SYSTEM DESIGN	34	-1	0
23	11531D0407	C0404	ADVANCES IN MANUFACTURING TECHNOLOGY	38	28	1
24	11851D6811	C3801	DIGITAL SYSTEM DESIGN	38	24	1
25	11851D6817	C5501	EMBEDDED SYSTEMS CONCEPTS	36	9	0
26	11991D5809	D5802	COMPUTER NETWORKS	38	-1	0
27	11991D5809	D5803	OBJECT ORIENTED SOFTWARE ENGINEERING	39	-1	0
28	11991D5809	D5807	WEB TECHNOLOGIES	34	-1	0
29	11991D5809	D5808	HUMAN COMPUTER INTERACTION	39	-1	0
30	11991D5809	D5809	DATA WARE HOUSING AND DATA MINING	36	-1	0
31	11991D5809	D5810	SECURED DATABASE APPLICATIONS DEVELOPMEN	36	0	0
32	11A31D5415	C4304	MICROCONTROLLER & APPLICATIONS	20	-3	0
33	11A51D5404	C4301	ELECTRICAL MACHINE MODELING AND ANALYSIS	18	-1	0
34	11A51D5404	C4302	ANALYSIS OF POWER ELECTRONIC CONVERTERS	18	-1	0
35	11A51D5404	C4304	MICROCONTROLLER & APPLICATIONS	14	-1	0
36	11A51D5703	C3801	DIGITAL SYSTEM DESIGN	40	30	1
37	11A51D5821	D5809	DATA WARE HOUSING AND DATA MINING	32	14	0
38	11AJ1D6809	C5504	VHDL-MODELING OF DIGITAL SYSTEMS	26	29	1
39	11B21D2515	D5809	DATA WARE HOUSING AND DATA MINING	27	12	0
40	11G51D4306	D4304	DIGITAL SIGNAL PROCESSING AND APPLICATIONS	34	8	0
41	11G51D4308	C4304	MICROCONTROLLER & APPLICATIONS	32	-1	0
42	11G51D4311	D4305	DIGITAL CONTROL SYSTEMS	30	1	0
43	11HE1D5807	C5801	DATASTRUCTURES AND ALGORTIHM ANALYSIS	34	1	0
44	11HE1D5807	C5802	MATHEMATICAL FOUNDATION OF COMPUTER SCI	34	1	0
45	11HE1D5807	C5803	COMPUTER ORGANIZATION AND ARCHITECTURE	37	1	0
46	11HE1D5807	C5804	DATABASE MANAGEMENT SYSTEMS	35	2	0
47	11HE1D5807	C5805	OPERATING SYSTEMS	36	0	0
48	11HE1D5807	C5806	OBJECT-ORIENTED PROGRAMMING	36	1	0

49	11HE1D5807	D5802	COMPUTER NETWORKS	31	4	0
50	11HE1D5807	D5803	OBJECT ORIENTED SOFTWARE ENGINEERING	33	0	0
51	11HE1D5807	D5807	WEB TECHNOLOGIES	30	1	0
52	11HE1D5807	D5808	HUMAN COMPUTER INTERACTION	30	0	0
53	11HE1D5807	D5809	DATA WARE HOUSING AND DATA MINING	35	0	0
54	11HE1D5807	D5810	SECURED DATABASE APPLICATIONS DEVELOPMENT	31	2	0
55	11KK1D5809	D5807	WEB TECHNOLOGIES	35	17	0
56	11L61D2502	D5809	DATA WARE HOUSING AND DATA MINING	33	-1	0
57	11S01D3803	C3802	VLSI-TECHNOLOGY AND DESIGN	35	8	0
58	11VV1D0514	C5801	DATASTRUCTURES AND ALGORTIHM ANALYSIS	33	18	0
59	11VV1D0514	C5806	OBJECT-ORIENTED PROGRAMMING	28	15	0
60	11VV1D0514	D5809	DATA WARE HOUSING AND DATA MINING	26	7	0
61	11W21D5821	C5801	DATASTRUCTURES AND ALGORTIHM ANALYSIS	36	15	0
62	11W21D5821	C5803	COMPUTER ORGANIZATION AND ARCHITECTURE	23	30	1
63	11W21D5821	C5806	OBJECT-ORIENTED PROGRAMMING	18	15	0
64	11W21D5821	D5807	WEB TECHNOLOGIES	38	17	0
65	11W21D5821	D5809	DATA WARE HOUSING AND DATA MINING	28	18	0
66	11X41D7009	D3802	ANALOG AND DIGITAL IC DESIGN	14	38	1
67	122W1D3809	C3802	VLSI-TECHNOLOGY AND DESIGN	31	-1	0
68	122W1D3809	C3805	ADVANCED DIGITAL SIGNAL PROCESSING	34	-1	0
69	12331D1506	C0401	FINITE ELEMENT METHODS	14	26	0
70	12351D3801	C3803	DIGITAL DATA COMMUNICATIONS	33	2	0
71	12351D3801	D3801	WIRELESS COMMUNICATION AND NETWORKS	26	7	0
72	12351D3801	D3802	ANALOG AND DIGITAL IC DESIGN	26	12	0
73	12351D3801	D3803	DSP PROCESSORS AND ARCHITECHTURE	26	19	0
74	12351D3801	D3804	OPTICAL COMMUNICATION AND NETWORKS	26	-1	0
75	12351D3801	D3805	RADAR SIGNAL PROCESSING	26	-1	0
76	12351D3801	D3807	INTERNET PROTOCOLS	29	5	0
77	12351D3801	D3809	ADVANCED SIGNAL PROCESSING LABORATORY	30	-1	0
78	12481D5524	C5502	EMBEDDED SYSTEM DESIGN	25	15	0
79	12521D8706	C8701	ADV. APPLIED MATHEMATICS	27	3	0
80	12531D5709	C5501	EMBEDDED SYSTEMS CONCEPTS	36	-1	0
81	12551D8704	D8701	FINITE ELEMENT METHOD IN STRUCTURALENGINE	34	3	0
82	12551D8707	C8701	ADV. APPLIED MATHEMATICS	33	0	0
83	12551D8710	D8703	STABILITY OF STRUCTURES	31	48	1
84	126B1D6807	D5505	LOW POWER VLSI DESIGN	35	32	1
85	126B1D6810	C5501	EMBEDDED SYSTEMS CONCEPTS	37	25	1
86	126N1D5809	D5809	DATA WARE HOUSING AND DATA MINING	19	-1	0
87	126Q1D5214	D4303	FLEXIBLE AC TRANSMISSION SYSTEMS	33	26	1
88	126Q1D5517	C3807	EMBEDDED & REAL TIME SYSTEMS	34	-1	0
89	126Q1D5517	C5501	EMBEDDED SYSTEMS CONCEPTS	33	-1	0
90	126Q1D5517	C5502	EMBEDDED SYSTEM DESIGN	32	-1	0
91	126Q1D5517	C5504	VHDL-MODELING OF DIGITAL SYSTEMS	31	-1	0
92	126Q1D5517	D0601	DESIGN OF FAULT TOLERANT SYSTEMS	31	-1	0
93	126Q1D5517	D5501	ALGORITHMS FOR VLSI DESIGN AUTOMATION	31	-1	0
94	126Q1D5517	D5502	REAL TIME OPERATING SYSTEMS FOR EMBEDDED S	30	-1	0
95	126Q1D5517	D5505	LOW POWER VLSI DESIGN	36	-1	0
96	126T1D2114	C2101	OPTIMIZATION TECHNIQUES & APPLICATIONS	17	29	0
97	12811D4301	C4304	MICROCONTROLLER & APPLICATIONS	34	-1	0
98	12811D4301	D4305	DIGITAL CONTROL SYSTEMS	32	-1	0
99	12811D4303	C4304	MICROCONTROLLER & APPLICATIONS	25	7	0
100	12811D4303	D4301	SWITCHED MODE POWER CONVERSION	30	12	0
101	12811D4307	D4305	DIGITAL CONTROL SYSTEMS	21	6	0
102	12811D4307	D4307	POWER QUALITY MANAGEMENT	21	22	0

103	12811D4314	C4301	ELECTRICAL MACHINE MODELING AND ANALYSIS	33	-1	0
104	12811D4314	D4302	POWER ELECTRONICS CONTROL OF AC DRIVES	29	-1	0
105	12811D4314	D4305	DIGITAL CONTROL SYSTEMS	35	-1	0
106	12811D5805	D5808	HUMAN COMPUTER INTERACTION	20	25	0
107	12811D5815	C5801	DATASTRUCTURES AND ALGORTIHM ANALYSIS	31	33	1
108	12811D5815	C5806	OBJECT-ORIENTED PROGRAMMING	37	13	0
109	12851D5820	D5811	MIDDLEWARE AND ENTERPRISE INTEGRATION TEC	35	-1	0
110	12851D5821	D5811	MIDDLEWARE AND ENTERPRISE INTEGRATION TEC	37	25	1
111	128K1D5604	C5607	ADVANCED DSP	30	6	0
112	129F1D5802	C5801	DATASTRUCTURES AND ALGORTIHM ANALYSIS	30	-1	0
113	129F1D6805	C5504	VHDL-MODELING OF DIGITAL SYSTEMS	38	24	1
114	129F1D6808	C5504	VHDL-MODELING OF DIGITAL SYSTEMS	37	31	1
115	129F1D6809	D5505	LOW POWER VLSI DESIGN	38	28	1
116	12A01D0407	C0401	FINITE ELEMENT METHODS	37	19	0
117	12A01D0407	D0403	ROBOTICS	38	21	0
118	12A01D3801	D3802	ANALOG AND DIGITAL IC DESIGN	39	-1	0
119	12A01D3802	D3802	ANALOG AND DIGITAL IC DESIGN	39	29	1
120	12A01D3803	C3803	DIGITAL DATA COMMUNICATIONS	38	24	1
121	12A01D3803	C3807	EMBEDDED & REAL TIME SYSTEMS	39	5	0
122	12A01D3806	C3803	DIGITAL DATA COMMUNICATIONS	39	8	0
123	12A01D3806	C3805	ADVANCED DIGITAL SIGNAL PROCESSING	39	-1	0
124	12A01D3806	C3807	EMBEDDED & REAL TIME SYSTEMS	39	-1	0
125	12A01D5402	C4305	MODERN CONTROL THEORY	39	-1	0
126	12A01D5823	C5801	DATASTRUCTURES AND ALGORTIHM ANALYSIS	40	7	0
127	12A01D5828	D5809	DATA WARE HOUSING AND DATA MINING	40	27	1
128	12A02D5406	D4304	DIGITAL SIGNAL PROCESSING AND APPLICATIONS	38	0	0
129	12A02D5815	D5809	DATA WARE HOUSING AND DATA MINING	39	21	0
130	12A31D0404	D0401	ADVANCED OPTIMIZATION TECHNIQUES	32	7	0
131	12A31D0404	D0403	ROBOTICS	32	6	0
132	12A31D0404	D0411	MECHANICS AND MANUFACTURING METHODS OF	34	8	0
133	12A31D5707	C3801	DIGITAL SYSTEM DESIGN	26	26	1
134	12A31D5707	C5504	VHDL-MODELING OF DIGITAL SYSTEMS	26	24	1
135	12A31D5707	D3803	DSP PROCESSORS AND ARCHITECHTURE	28	19	0
136	12A31D5707	D5505	LOW POWER VLSI DESIGN	26	18	0
137	12A31D5714	C5502	EMBEDDED SYSTEM DESIGN	34	17	0
138	12A31D5714	D5505	LOW POWER VLSI DESIGN	26	20	0
139	12A71D3805	C3801	DIGITAL SYSTEM DESIGN	23	-1	0
140	12A71D3807	C3801	DIGITAL SYSTEM DESIGN	36	-1	0
141	12A71D3809	C3801	DIGITAL SYSTEM DESIGN	34	-1	0
142	12A71D5619	C5201	POWERSYSTEM OPERATION AND CONTROL	17	10	0
143	12A71D5619	C5603	HVDC TRANSMISSIONS	20	21	0
144	12B21D4306	C4302	ANALYSIS OF POWER ELECTRONIC CONVERTERS	32	21	0
145	12B21D4306	D4305	DIGITAL CONTROL SYSTEMS	36	-1	0
146	12BQ1D3811	C3803	DIGITAL DATA COMMUNICATIONS	31	-1	0
147	12BQ1D3811	C3805	ADVANCED DIGITAL SIGNAL PROCESSING	30	-1	0
148	12GK1D5702	C3801	DIGITAL SYSTEM DESIGN	39	-1	0
149	12GK1D5702	C5504	VHDL-MODELING OF DIGITAL SYSTEMS	37	-1	0
150	12GK1D5703	C5501	EMBEDDED SYSTEMS CONCEPTS	37	17	0
151	12GK1D5705	C3801	DIGITAL SYSTEM DESIGN	38	34	1
152	12H41D5602	C5607	ADVANCED DSP	35	6	0
153	12HE1D5807	C5804	DATABASE MANAGEMENT SYSTEMS	34	27	1
154	12HE1D5807	D5809	DATA WARE HOUSING AND DATA MINING	38	18	0
155	12HU1D5609	C5609	REACTIVE POWER COMPENSATION & MANAGEME	15	24	0
156	12HU1D5610	C5201	POWERSYSTEM OPERATION AND CONTROL	17	24	0

157	12HU1D5610	C5606	AI TECHNIQUES	19	31	1
158	12HU1D5611	C5201	POWERSYSTEM OPERATION AND CONTROL	26	15	0
159	12JG1D6810	C5504	VHDL-MODELING OF DIGITAL SYSTEMS	17	33	1
160	12JP1D5606	D5603	SURGE PHENOMENON & INSULATION COORDINATION	32	13	0
161	12K11D5717	C3801	DIGITAL SYSTEM DESIGN	29	-1	0
162	12KG1D3801	C3801	DIGITAL SYSTEM DESIGN	37	36	1
163	12MC1D5807	C5801	DATASTRUCTURES AND ALGORITHM ANALYSIS	38	18	0
164	12MC1D5807	D5803	OBJECT ORIENTED SOFTWARE ENGINEERING	30	24	1
165	12MC1D5807	D5809	DATA WARE HOUSING AND DATA MINING	31	3	0
166	12ME1D4301	D4304	DIGITAL SIGNAL PROCESSING AND APPLICATIONS	24	10	0
167	12ME1D4310	D4305	DIGITAL CONTROL SYSTEMS	31	11	0
168	12ME1D5708	D5505	LOW POWER VLSI DESIGN	35	34	1
169	12ME1D5811	C5801	DATASTRUCTURES AND ALGORITHM ANALYSIS	8	7	0
170	12MG1D5613	C5606	AI TECHNIQUES	34	13	0
171	12MG1D5616	C5201	POWERSYSTEM OPERATION AND CONTROL	35	4	0
172	12MG1D5616	D4305	DIGITAL CONTROL SYSTEMS	37	3	0
173	12NF1D5703	C3802	VLSI-TECHNOLOGY AND DESIGN	34	16	0
174	12NH1D5716	C3802	VLSI-TECHNOLOGY AND DESIGN	32	13	0
175	12NH1D5716	D5505	LOW POWER VLSI DESIGN	37	20	0
176	12NQ1D5518	D5505	LOW POWER VLSI DESIGN	37	12	0
177	12NR1D5702	D5506	CPLD AND FPGA ARCHITECTURE AND APPLICATIONS	38	25	1
178	12NR1D5706	D5501	ALGORITHMS FOR VLSI DESIGN AUTOMATION	34	9	0
179	12NR1D5706	D5505	LOW POWER VLSI DESIGN	34	8	0
180	12NR1D5709	D5505	LOW POWER VLSI DESIGN	38	29	1
181	12NR2D5706	D5506	CPLD AND FPGA ARCHITECTURE AND APPLICATIONS	37	-1	0
182	12Q71D5702	C3802	VLSI-TECHNOLOGY AND DESIGN	38	15	0
183	12Q71D5702	C5501	EMBEDDED SYSTEMS CONCEPTS	33	23	0
184	12U41D4517	D3805	RADAR SIGNAL PROCESSING	38	16	0
185	12W21D5820	C5801	DATASTRUCTURES AND ALGORITHM ANALYSIS	18	17	0
186	12W21D5820	C5804	DATABASE MANAGEMENT SYSTEMS	18	24	0
187	12W21D5820	D5808	HUMAN COMPUTER INTERACTION	28	26	1
188	12W32D1508	D1502	EXPERIMENTAL STRESS ANALYSIS	38	16	0
189	12X21D5502	C3802	VLSI-TECHNOLOGY AND DESIGN	36	7	0
190	12X21D5510	C5501	EMBEDDED SYSTEMS CONCEPTS	37	20	0
191	12X21D5510	D5501	ALGORITHMS FOR VLSI DESIGN AUTOMATION	38	16	0
192	12X21D5511	C3807	EMBEDDED & REAL TIME SYSTEMS	39	-1	0
193	12X21D5513	D5501	ALGORITHMS FOR VLSI DESIGN AUTOMATION	38	-1	0

Note:1)For Recounting/Revaluation/Challenge By Revaluation Apply through Online(www.jntukresults.edu.in)

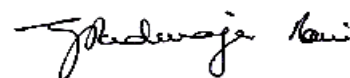
NOTE:2) Last Date for Apply Recounting/Revaluation/Challenge By Revaluation: **29-12-2017**

NOTE:3)

-1 in the filed of externals indicates student absent for the respective subject.

-2 in the filed of externals indicates student result is withheld for the respective subject.

-3 in the filed of externals indicates Malpractice for the respective subject.



Date: **13-12-2017**

Controller of Examinations(PG)